

ABSTRACT

A rear substrate of plasma display panel comprises a substrate, a plurality of address electrodes, a plurality of auxiliary address electrodes, a rib and a fluorescent layer. The rib is disposed on the substrate to define a plurality of discharge spaces. Each of the address electrodes is disposed in each of the discharge spaces. The auxiliary address electrodes are disposed between the substrate and the rib. When an address signal is inputted to the address electrodes, the auxiliary address electrodes are grounded for reducing the probability of error discharge. During sustain period, the auxiliary address electrodes are coupled to a positive voltage for preventing ion bombardment of phosphors layer.